EXHIBIT 9



(12) United States Patent

Falster

(10) Patent No.:

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(45) Date of Patent:

Jan. 29, 2002

SILICON ON INSULATOR STRUCTURE HAVING A LOW DEFECT DENSITY HANDLER WAFER AND PROCESS FOR THE PREPARATION THEREOF

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Subject to any disclaimer, the term of this (*) Notice: patent is extended or adjusted under 35

U.S.C. 154(b) by 0 days.

(21) Appl. No.: 09/737,715

(22) Filed: Dec. 15, 2000

Related U.S. Application Data

- Continuation of application No. 09/387,288, filed on Aug. 31, 1999, now Pat. No. 6,236,104. Provisional application No. 60/098,902, filed on Sep. 2,
- (60)

(51)	Int. Cl. ⁷	H01L 29/06; H01L 27/01
` '		H01L 27/12; H01L 31/0392

- (52) U.S. Cl. 257/618; 257/347; 257/617
- 257/913, 617

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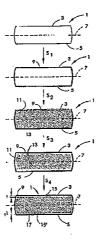
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Primary Examiner-Ngân V. Ngô (74) Attorney, Agent, or Firm-Senniger, Powers, Leavitt & Roedel

ABSTRACT (57)

The present invention relates to a process for the preparation of a silicon on insulator wafer. The process including implanting oxygen in a single crystal silicon wafer having an axially symmetric region in which there is a predominant intrinsic point defect which is substantially free of agglomerated intrinsic point defects. Additionally, the present invention relates to a silicon on insulator ("SOI") structure in which the device layer and the handle wafer each have an axially symmetric region which is substantially free of agglomerated intrinsic point defects. Additionally, the present invention is directed to such a SOI structure in which the handle wafer is capable of forming an ideal, non-uniform depth distribution of oxygen precipitates upon being subjected to the heat treatment cycles of essentially any arbitrary electronic device manufacturing process.

42 Claims, 35 Drawing Sheets



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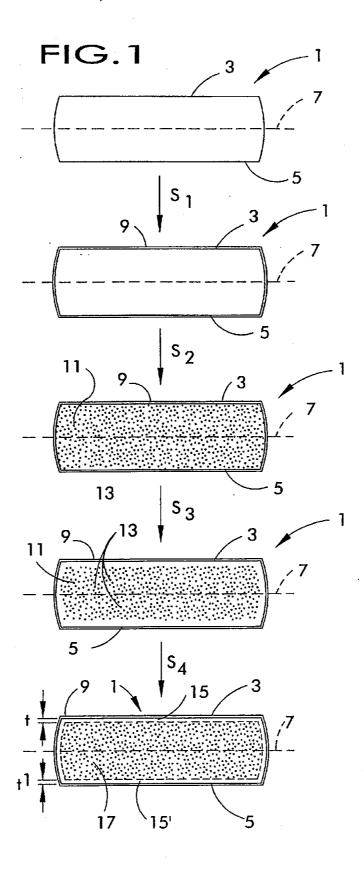
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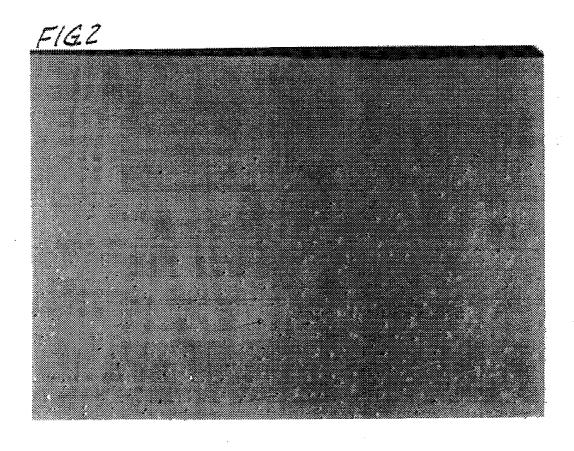
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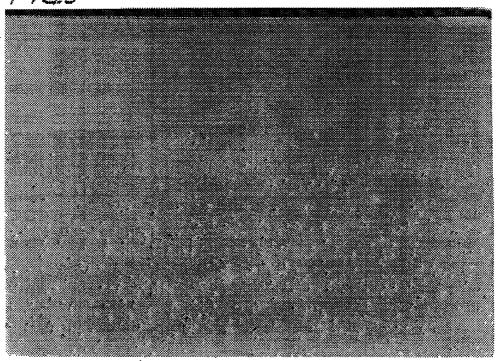
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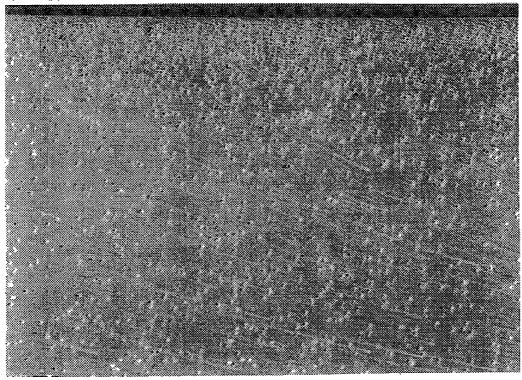




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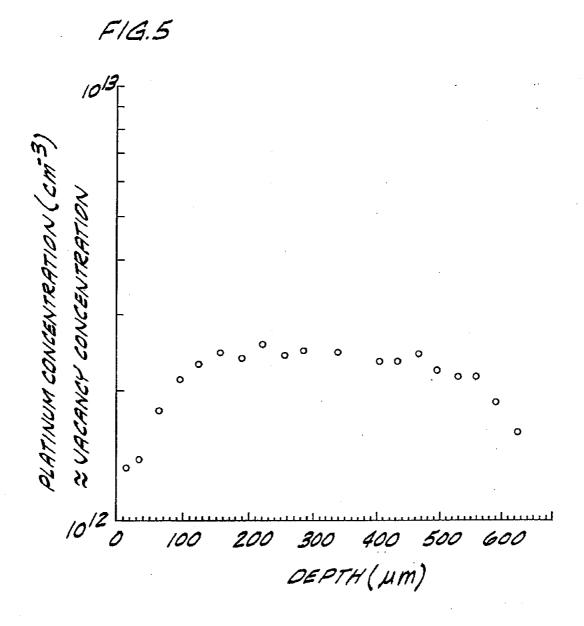
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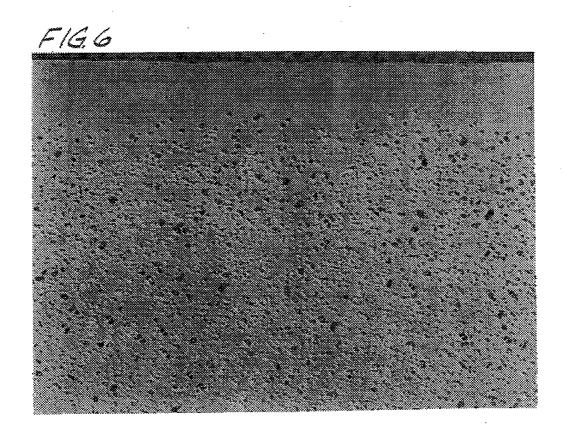
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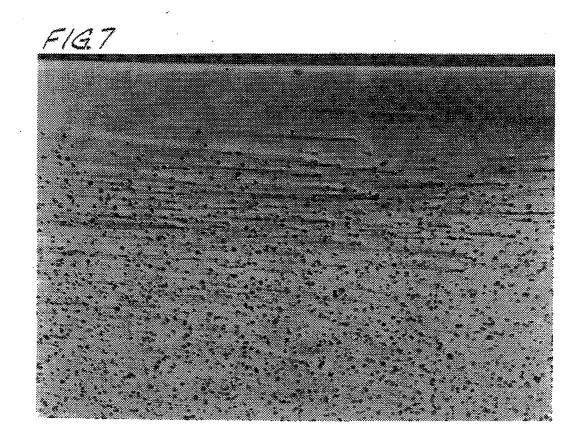
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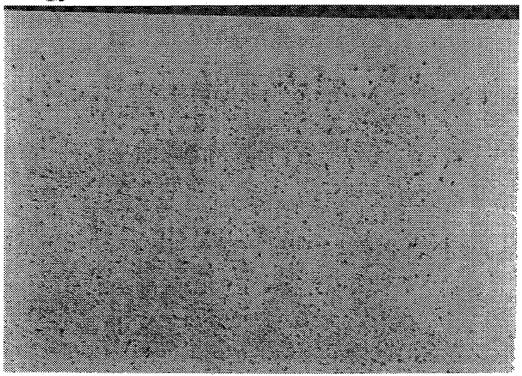
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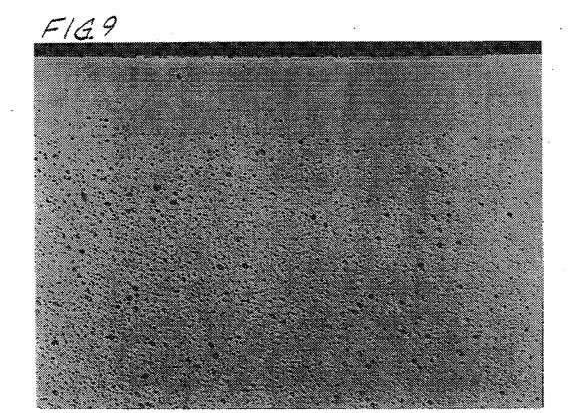
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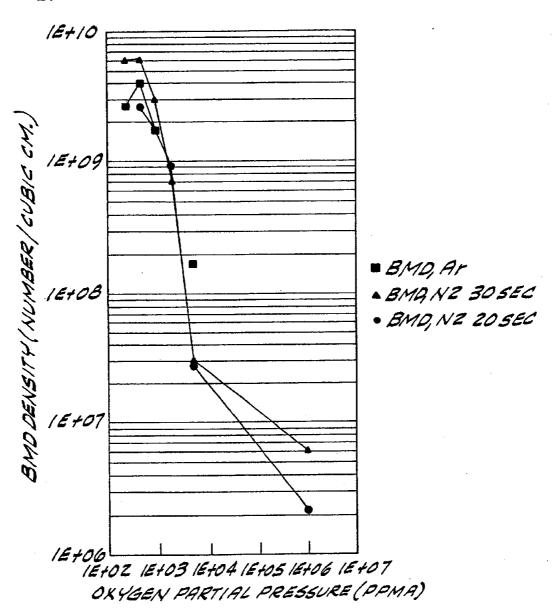
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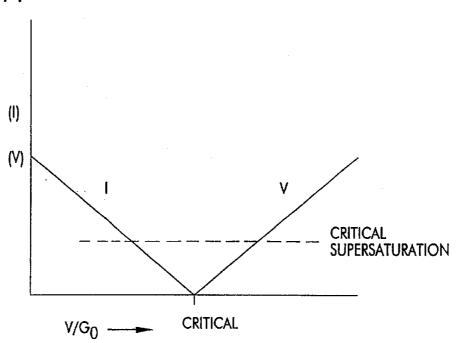
FIG. 10 BMD DENSITY US. OXYGEN PARTIAL PRESSURE

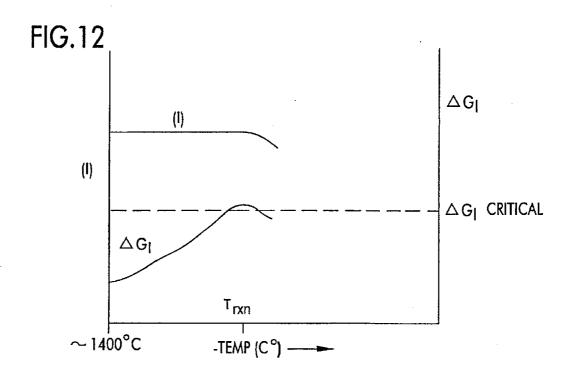


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FIG.11





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FIG.13

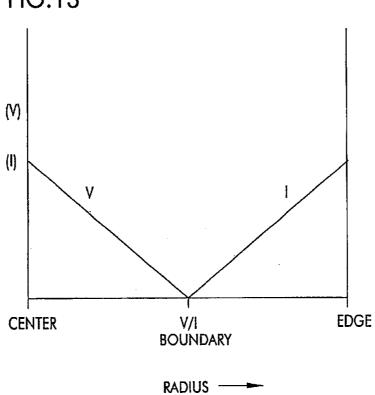
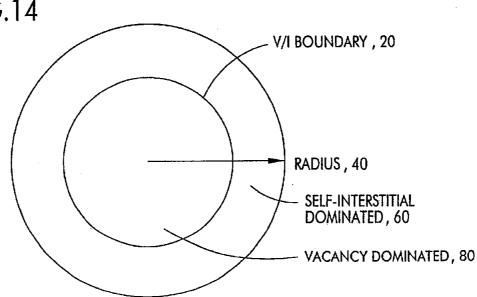
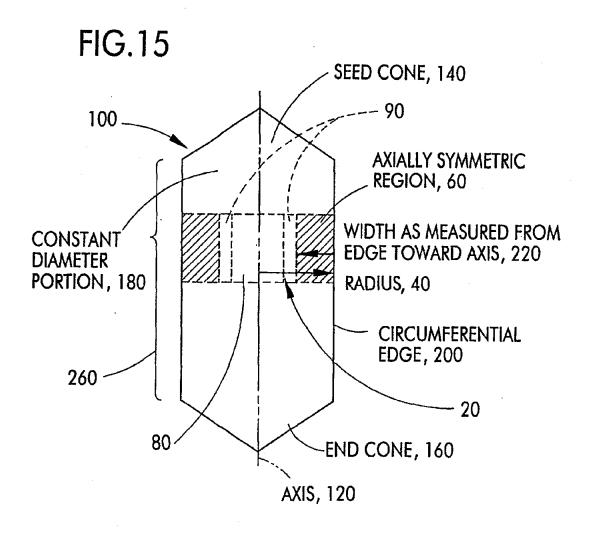


FIG.14



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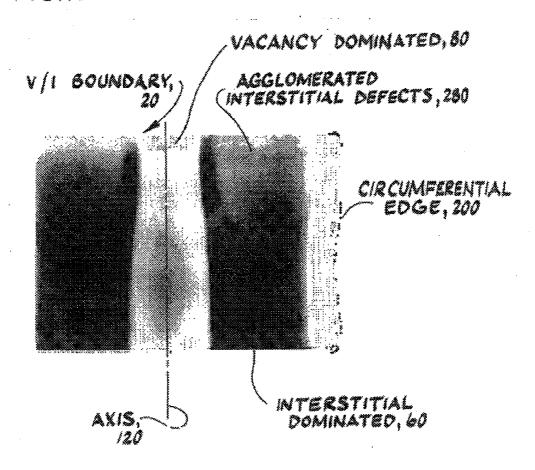
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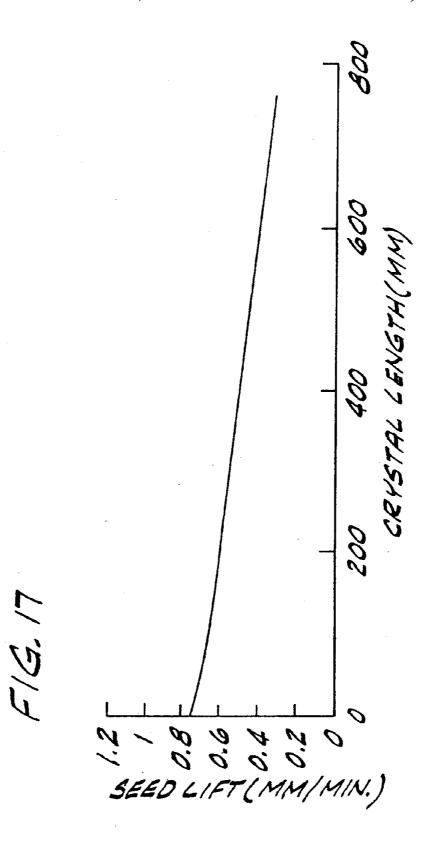
FIG. 16



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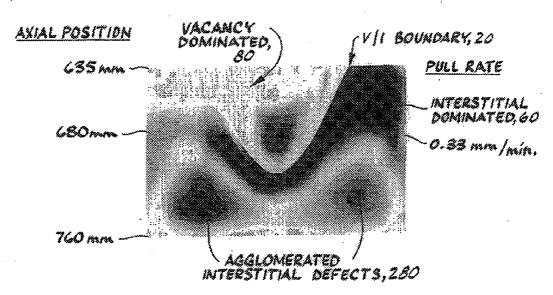


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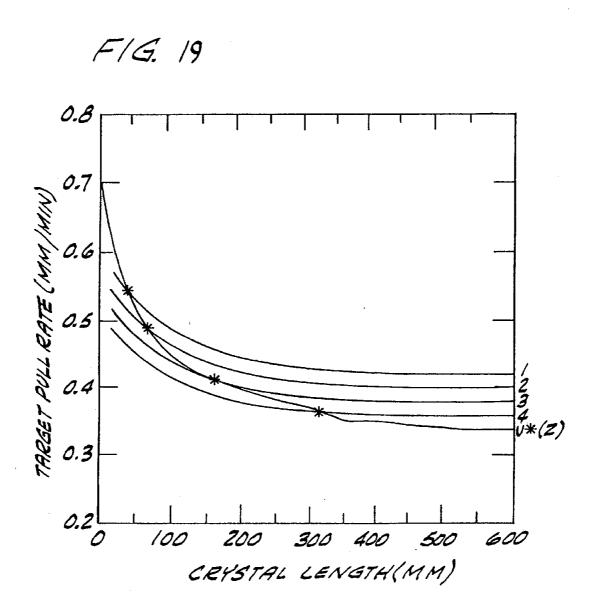
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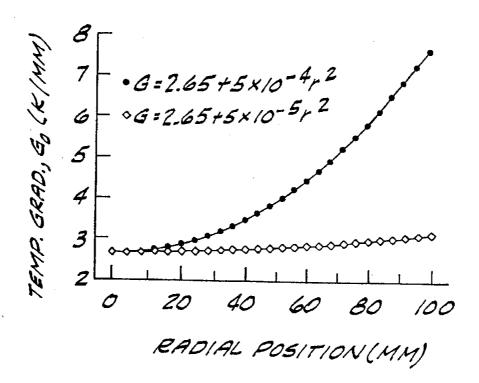


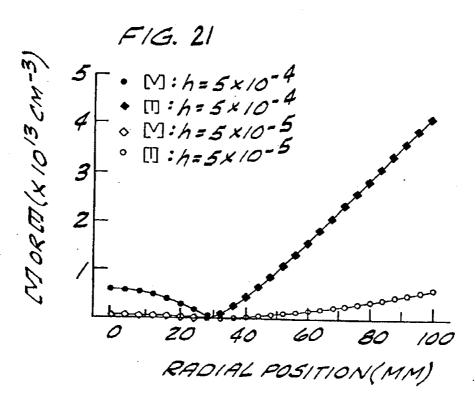
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FIG. 20

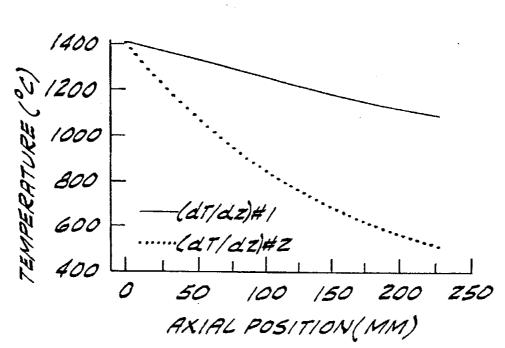


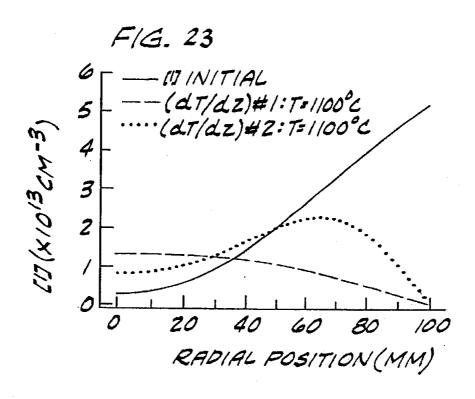


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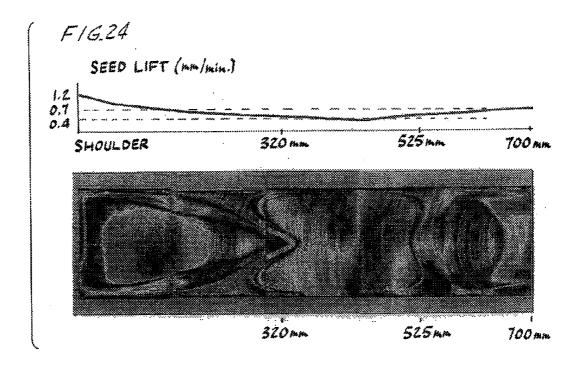






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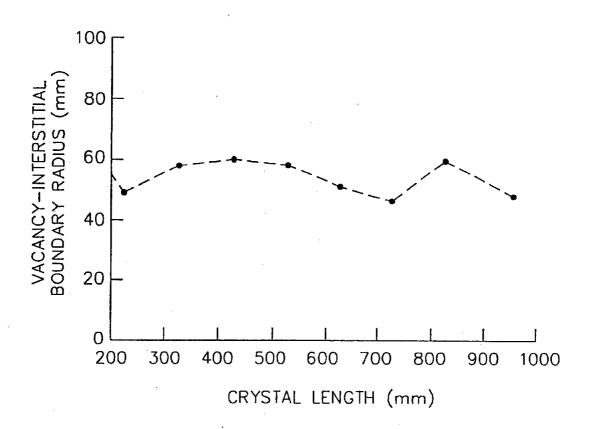
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FIG. 25

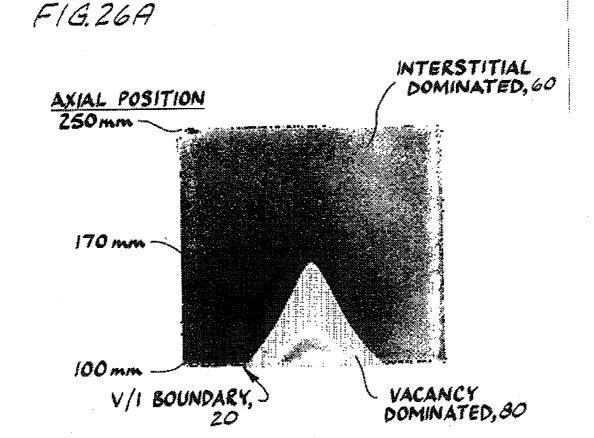


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VACANCY DOMINATED, 80



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FIG. 26B

